

1c979 U.S. PTO

09/874044



708	Class	Subclass	ISSUE CLASSIFICATION
496			

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>[Signature]</i> O.A. <i>15</i>	PATENT DATE
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APPLICATION NO. 09/874044	CONT/PRIOR F	CLASS 708	SUBCLASS <i>496</i>	ART UNIT 2121	EXAMINER <i>MAI</i>
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APPLICANTS

Graham Kirsch

496

TITLE

Method and circuit for normalization of floating point significands in a SIMD array MPP

PTO-2040  
12/99

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
708	496	708	205				
INTERNATIONAL CLASSIFICATION							
G06F	7/38						
G06F	7/00						

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. <i>5</i>	Figs. Drwg. <i>6</i>	Print Fig. <i>3</i>	Total Claims <i>39</i>	Print Claim for O.G. <i>1</i>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b> <i>2/22/05</i>	
	TAN V. MAI PRIMARY EXAMINER <i>[Signature]</i> _____ (Primary Examiner) (Date) <i>2/2005</i>			<b>ISSUE FEE</b> Amount Due <i>1400.00</i> Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	
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